

## Verilog Interview Questions And Answers

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VERILOG INTERVIEW QUESTIONS WITH ANSWERS!. Timing delays between pins can be expressed in greater detail by specifying rise, fall, and turn-off delay values. One, two, three, six, or twelve delay values can be specified for any path. The order in which the delay values are specified must be strictly followed.

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These are very Basic Verilog Interview Questions and Answers for freshers and experienced both. Q1: Difference Between Task And Function? A1: Function: A function is unable to enable a task however functions can enable other functions. A function will carry out its required duty in zero simulation time.

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Verilog interview Questions 22) Will case infer priority register if yes how give an example? yes case can infer priority register depending on coding style reg r; // Priority encoded mux, always @ (a or b or c or select2) begin r = c; case (select2) 2'b00: r = a; 2'b01: r = b; endcase end Verilog interview Questions

Verilog interview Questions & answers - ASIC

(Verilog interview questions that is most commonly asked) The Verilog language has two forms of the procedural assignment statement: blocking and non-blocking. The two are distinguished by the = and <= assignment operators.

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FUNCTIONAL VERIFICATION QUESTIONS (Q i1) Explain how to inject a crc error in a packet which has just data and crc fields. Ans: A crc error can be injected by modifying the crc value only. If the data is modified to inject a crc error, then it may end up in a situation where the new modified packet may have the same crc.

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(Verilog interview questions that is most commonly asked) The Verilog language has two forms of the

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procedural assignment statement: blocking and non-blocking. The two are distinguished by the = and <= assignment operators.

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counter is a type of counter composed of a circular shift register. The output of the last shift register is fed to the input of the first ...

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Interview Questions in Verilog 1. What is the difference between wire and reg? Table: Difference between Wire and reg

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